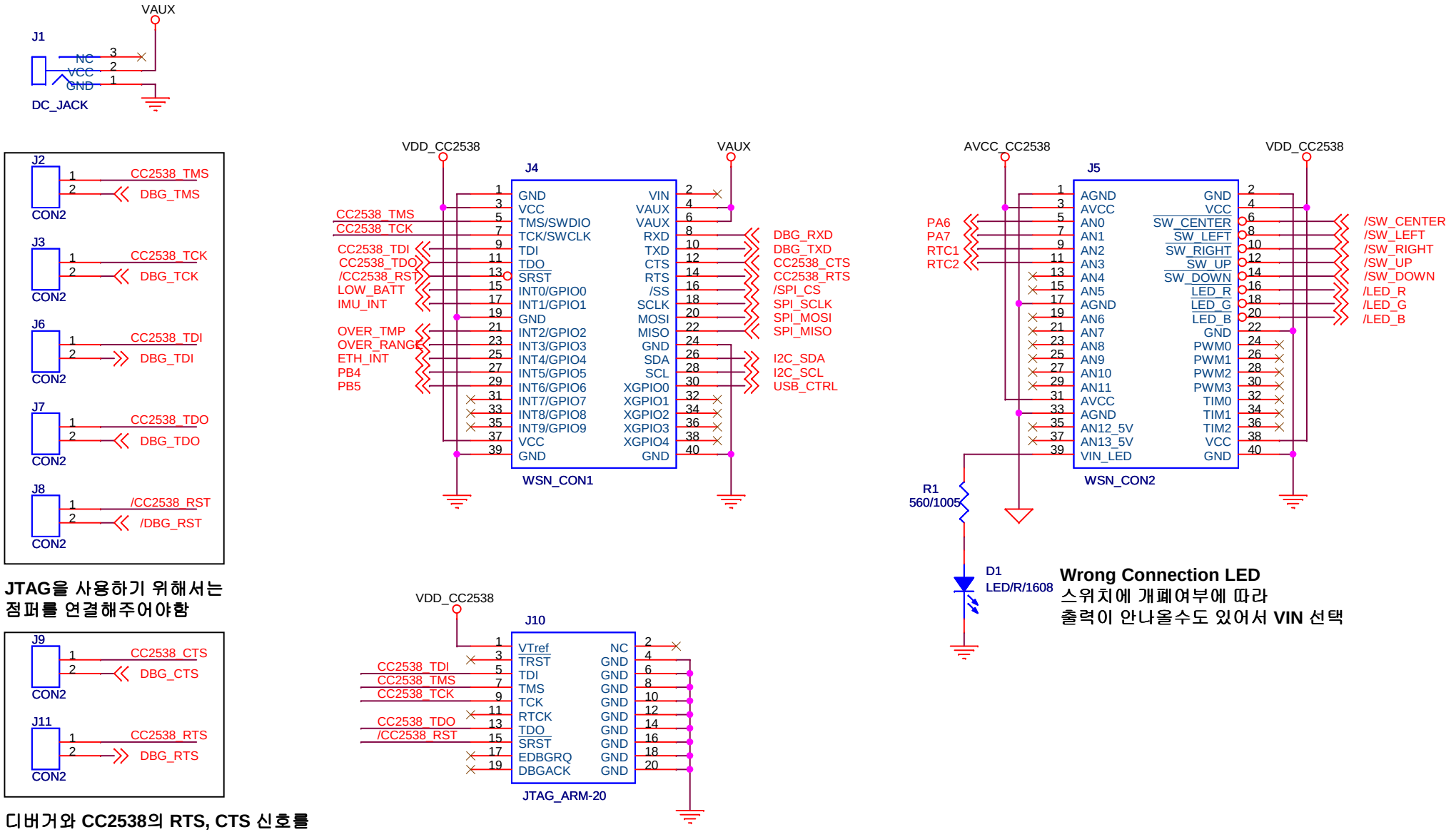
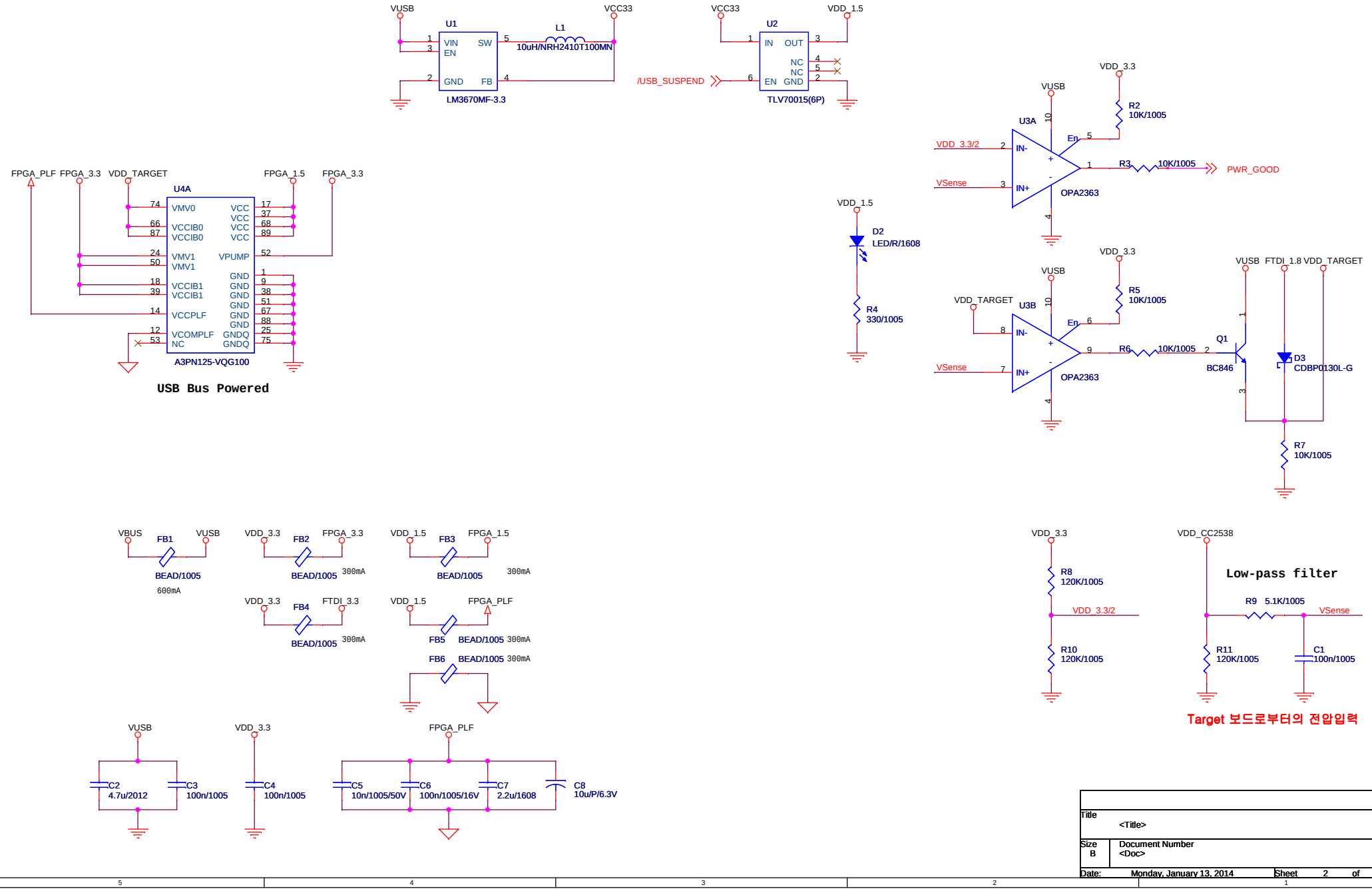
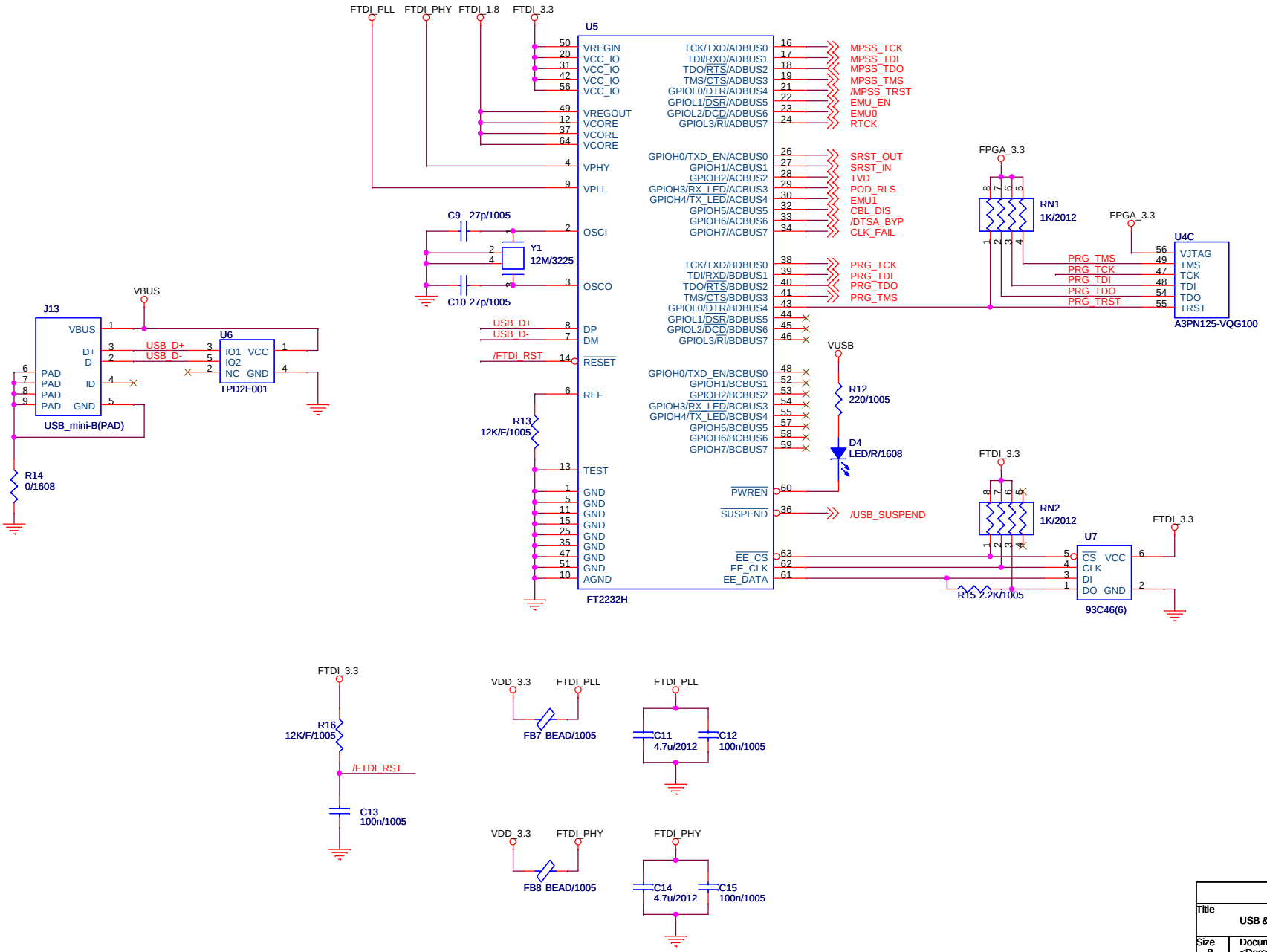
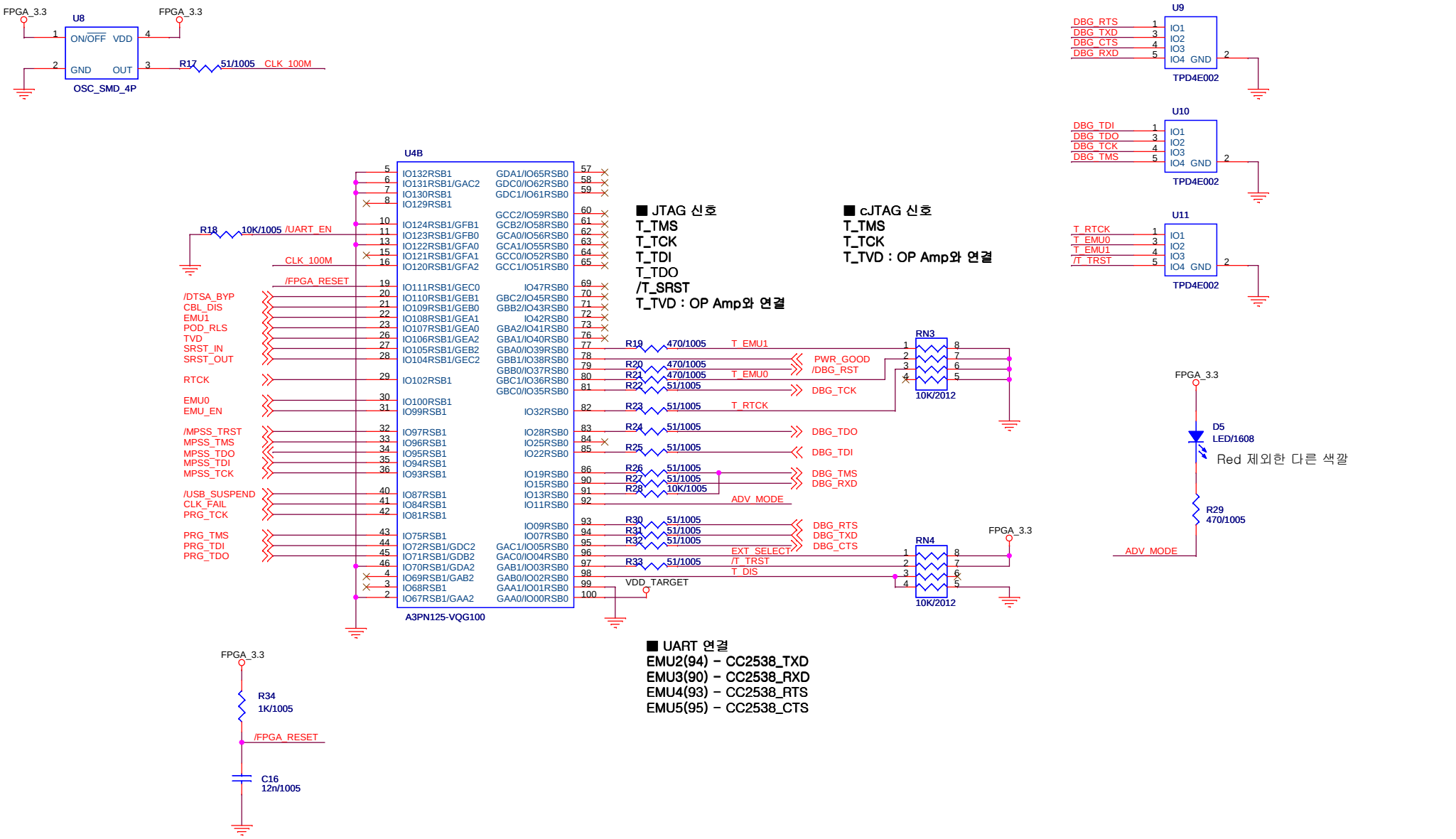


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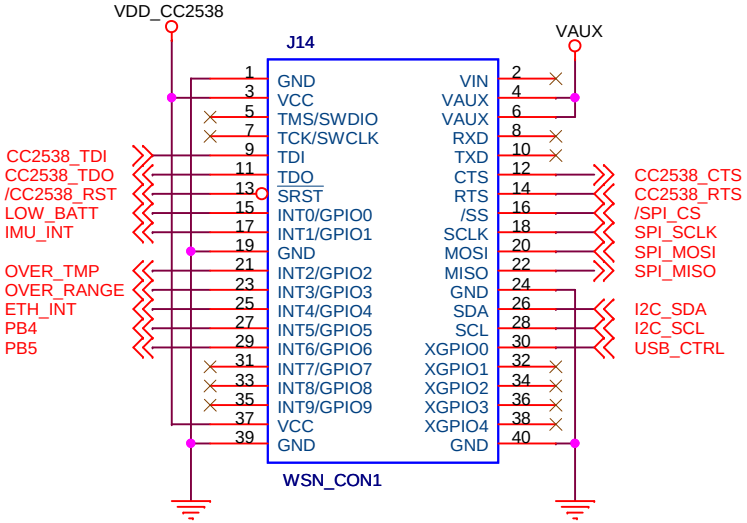

VUSB
U1
VIN
EN
GND
SW
FB
L1
10uH/NRH2410T100MN
VCC33
LM3670MF-3.3
VCC33
VDD_1.5
U2
IN
OUT
NC
NC
EN
GND
/USB_SUSPEND
TLV70015(6P)
FPGA_PLF
FPGA_3.3
VDD_TARGET
FPGA_1.5
FPGA_3.3
U4A
VMV0
VCC
VCCIB0
VMV1
VPUMP
GND
VCCIB1
VCCPLF
VCOMPLF
NC
GNDQ
A3PN125-VQG100
USB Bus Powered
VDD_1.5
D2
LED/R/1608
R4
330/1005
VDD_3.3
VUSB
U3A
VDD_3.3/2
VSense
IN-
IN+
En
OPA2363
R2
10K/1005
R3
10K/1005
PWR_GOOD
VDD_TARGET
U3B
R5
10K/1005
R6
10K/1005
Q1
BC846
D3
CDBP0130L-G
R7
10K/1005
VBUS
FB1
BEAD/1005
600mA
FB2
FB3
FB4
FB5
FB6
300mA
FTDI_3.3
FPGA_PLF
C2
4.7u/2012
C3
100n/1005
C4
100n/1005
C5
10n/1005/50V
C6
100n/1005/16V
C7
2.2u/1608
C8
10u/P/6.3V
R8
120K/1005
R10
120K/1005
VDD_CC2538
Low-pass filter
R9
5.1K/1005
R11
120K/1005
C1
100n/1005
Target 보드로부터의 전압입력
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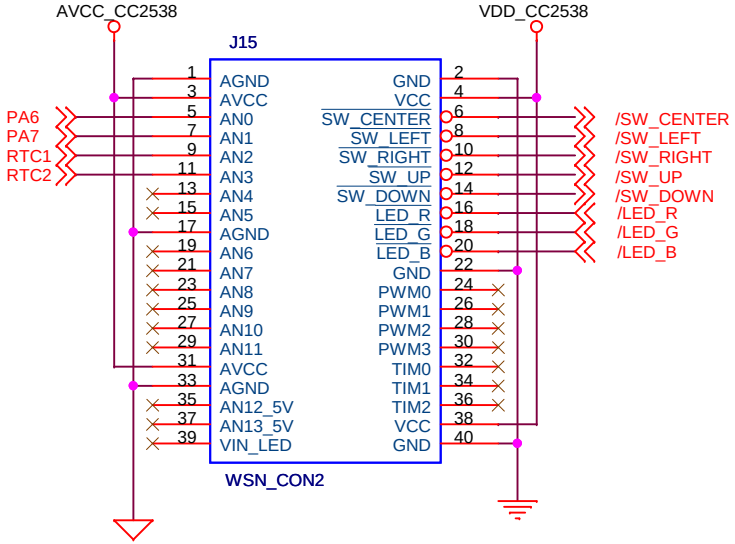


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